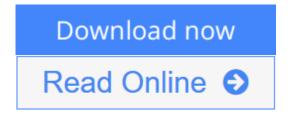
# GBTU & MTU Microprocessor

#### INTRODUCTION TO MICROPROCESSORS

By A P Godse, D A Godse



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Digital Computer and Microprocessor: Digital Computers: General architecture and brief description of elements, Instruction execution, Instruction format, And instruction set, Addressing modes, Programming system, Higher level languages. Buses and CPU Timings: Bus size and signals, Machine cycle timing diagram, Instruction timing, Processor timing. Microprocessor and Microprocessor Development Systems: Evolution of microprocessor, Microprocessor architecture and its operations, Memory, Inputs-outputs (I/Os), Data transfer schemes interfacing devices, Architecture advancements of microprocessors, Typical microprocessor development system. 8-bit Microprocessors: 8085 microprocessor: Pin configuration, Internal architecture. Timing and signals: Control and status, Interrupt: ALU, Machine cycles. Instruction Set of 8085: Addressing Modes: Register addressing, Direct addressing; Register indirect addressing, Immediate addressing, And implicit addressing. Instruction format, Op-codes, Mnemonics, No. of bytes, RTL, Variants, No. of machine cycles and T states, Addressing modes. Instruction Classification: Data transfer, Arithmetic operations, Logical operations, Branching operation, Machine control; Writing assembly language programs, Assembler directives. 16-bit Microprocessors: Architecture: Architecture of INTEL 8086 (Bus interface unit, Execution unit), Register organization, Memory addressing, Memory segmentation, Operating modes. Instruction Set of 8086: Addressing modes: Instruction format: Discussion on instruction set: Groups: Data transfer, Arithmetic, Logic string, Branch control transfer, Processor control. Interrupts: Hardware and software interrupts, Responses and types. Fundamental of Programming: Development of algorithms, Flowcharts in terms of structures, (series, parallel, if-then-else etc.) Assembler Level Programming: Memory space allocation (mother board and user program) Assembler level programs (ASMs). Peripheral Interfacing: I/O programming: Programmed I/O, Interrupt driven I/O, DMA I/O interface: serial and parallel communication, Memory I/O mapped I/Os. Peripheral Devices: 8237 DMA controller, 8255-Programmable peripheral interface, 8253/8254 Programmable timer/counter. 8259 Programmable interrupt controller.

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